

## FPGA BASED CONTROL SINGLE PHASE THREE LEVEL INVERTER

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### ABSTRACT

The three level inverters are implemented using digital logic that determines all required switching sequences and the necessary switching sequence by dividing the timings using FPGA. The FPGA can be programmed using Verilog which is straight forward technique which is cost effective for single chip implementation even for very high switching frequencies.

**KEYWORDS:** FPGA, VHDL, CPLD, XLINX